AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently amended) An information processing apparatus, comprising:

~~storage programmed logic circuitry~~a memory for storing data to display a plurality of windows and data to display a plurality of selection areas which respectively correspond to said plurality of windows,

a display for including a first display area on which only a predetermined window out of the plurality of windows is displayed or the plurality of windows are displayed in an overlapping manner and a second display area on which said plurality of selection areas are displayed, and

a processor coupled to the memory, the memory storing instructions that, when executed by the processor, control the processor to:

detect~~detector for detecting~~ an input to display positions of said plurality of selection areas, and

~~a first display controller for displaying~~display, when it is determined that a first predetermined input is performed within a selection area corresponding to a window displayed on said first display area or a forefront window out of the plurality of windows displayed in the overlapping manner on said first display area by said detector, a window corresponding to the selection area that said detector detects as the first predetermined input on said second display area.

2. (Currently amended) An information processing apparatus according to claim 1, ~~further comprising a second display controller for displaying~~wherein the processor is further

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controlled to display, when it is determined that a first predetermined input is performed within a selection area corresponding to a window which is not displayed on said first display area and said second display area or a window a part of which is hidden under the window displayed on the forefront on said first display area ~~by said detector~~, the window corresponding to the selection area on said first display area or on the forefront on said first display area.

3. (Currently amended) An information processing apparatus according to claim 1, wherein the processor is further controlled to display~~further comprising a third display controller for displaying~~, when it is determined that a second predetermined input is performed within a selection area corresponding to a window which is not displayed on said first display area and said second display area or a window a part of which is hidden under the window displayed on the forefront on said first display area ~~by said detector~~, the window corresponding to the selection area on said second display area.

4. (Currently amended) An information processing apparatus, comprising:

~~storage programmed logic circuitry~~a memory for storing data to display a plurality of windows and data to display a plurality of selection areas which respectively correspond to said plurality of windows,

a display for including a first display area on which only a predetermined window out of the plurality of windows is displayed or said plurality of windows are displayed in an overlapping manner and a second display area on which said plurality of selection areas are displayed, and

a processor coupled to the memory, the memory storing instructions that, when executed

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by the processor, control the processor to:

a detector for detecting~~detect~~ an input to display positions of said plurality of selection

areas, and

a third display controller for displaying~~display~~, when it is determined that a second

predetermined input is performed at a display position of a selection area corresponding to a

window which is not displayed on said first display area and said second display area or a

window a part of which is hidden under a forefront window out of the plurality of windows

displayed in the overlapping manners on said first display area ~~by said detector~~, a window

corresponding to the selection area that ~~said detector~~ is detected as the~~a~~ first predetermined input

on said second display area.

5. (Currently amended) An information processing apparatus according to claim 4,

wherein the processor is further controlled to display~~further comprising a first display controller~~

~~for displaying~~, when it is determined that a~~the~~ first predetermined input is performed within a

selection area corresponding to a window displayed on said first display area or the window

displayed on the forefront ~~by said detector~~, the window corresponding to the selection area on

said second display area.

6. (Currently amended) An information processing apparatus according to claim 3,

wherein the processor is further controlled to:

detect~~said detector detects~~ an input to an arbitrary position of said second display area,

and

~~further comprising a setter for setting~~set, when a window is displayed on said second

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display area ~~by said first display controller or said third display controller~~, the window to an

inputable state ~~from said detector~~.

7. (Currently amended) An information processing apparatus according to claim 1,

wherein the processor is further controlled to display~~further comprising a fourth display~~

~~controller for displaying~~, when it is determined that a predetermined input is performed within a

selection area corresponding to the window displayed on said second display area, the window

corresponding to the selection area of the forefront on said first display area.

8. (Currently amended) An information processing apparatus according to claim 1,

wherein the processor is further controlled to display~~further comprising a fifth display controller~~

~~for displaying~~, in a case that said window is displayed on said second display area and when it is

determined that other window is being displayed on said second display area, the other window

on the forefront on said first display area.

9. (Currently amended) An information processing apparatus according to claim 1,

wherein the processor is further controlled to detect~~wherein said detector detects~~ said first

predetermined input on the basis of the input from a touch panel which is not set on said first

display area but is set on said second display area.

10. (Currently amended) An information processing apparatus according to claim 1,

wherein said ~~storage programmed logic circuitry~~memory stores data to display a basic input

window to be displayed on said second display area, and

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the processor is further controlled to display~~further comprising a basic display controller for displaying~~ said basic input window on said second display area when no window to be displayed on said second display area is present.

11. (Currently amended) An information processing apparatus according to claim 1, wherein the processor is further controlled to~~further comprising generating programmed logic circuitry for~~, when a predetermined coordinates input is performed to said window displayed on said second display area, ~~generating~~ generate data to display a new window and data to display a new selection area, and ~~storing~~ store the generated data in said ~~storage programmed logic circuitry~~memory by bringing the data to display a new window and the data to display a new selection area into correspondence with each other, and

the processor is further controlled to display~~a selection area display controller for displaying~~ said generated selection area ~~generated by said generating programmed logic circuitry~~ on said second display area.

12. (Currently amended) An information processing program of an information processing apparatus comprising ~~storage programmed logic circuitry~~a memory for storing data to display a plurality of windows and data to display a plurality of selection areas which respectively correspond to said plurality of windows, and a display for including a first display area on which only a predetermined window out of the plurality of windows is displayed or said plurality of windows are displayed in an overlapping manner, and a second display area on which said plurality of selection areas are displayed, causing a processor of said information processing apparatus to execute

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detecting an input to display positions of said plurality of selection areas, and

displaying, when it is determined that a first predetermined input is performed within a

selection area corresponding to a window displayed on said first display area or a forefront

window out of a plurality of windows displayed in the overlapping manner on said first display

area, a window corresponding to the selection area that is detected as the first predetermined

input on said second display area.

13. (Currently amended) A storage medium storing an information processing program

of an information processing apparatus comprising ~~storage programmed logic circuitry~~a memory

for storing data to display a plurality of windows and data to display a plurality of selection areas

which respectively correspond to said plurality of windows, and a display for including a first

display area on which only a predetermined window out of the plurality of windows is displayed

or the plurality of windows are displayed in an overlapping manner, and a second display area on

which said plurality of selection areas are displayed, wherein

said information processing program causes a processor of said information processing

apparatus to execute

detecting an input to display positions of said plurality of selection areas, and

displaying, when it is determined that a first predetermined input is performed within a

selection area corresponding to a window displayed on said first display area or a forefront

window out of a plurality of windows displayed in the overlapping manner on said first display

area, a window corresponding to the selection area that is detected as the first predetermined

input on said second display area.

14. (Currently amended) A window controlling method of an information processing apparatus comprising ~~storage programmed logic circuitry~~a memory for storing data to display a plurality of windows and data to display a plurality of selection areas which respectively correspond to said plurality of windows, and a display for including a first display area on which only a predetermined window out of the plurality of windows is displayed or the plurality of windows are displayed in an overlapping manner, and a second display area on which said plurality of selection areas are displayed, further including:

detecting an input to display positions of said plurality of selection areas, and

displaying, when it is determined that a first predetermined input is performed within a selection area corresponding to a window displayed on said first display area or a forefront window out of the plurality of windows displayed in the overlapping manner on said first display area, a window corresponding to the selection area detected as the first predetermined input on said second display area.

15. (Currently amended) An information processing program of an information processing apparatus comprising ~~storage programmed logic circuitry~~a memory for storing data to display a plurality of windows and data to display a plurality of selection areas which respectively correspond to said plurality of windows, and a display for including a first display area on which only a predetermined window out of the plurality of windows is displayed or the plurality of windows are displayed in an overlapping manner, and a second display area on which said plurality of selection areas are displayed, causing a processor of said information processing apparatus to execute

detecting an input to display positions of said plurality of selection areas, and

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displaying, when it is determined that a second predetermined input is performed at a

display position of a selection area corresponding to a window which is not displayed on said

first display area and said second display area or a window a part of which is hidden under a

forefront window out of the plurality of windows displayed in the overlapping manner displayed

on said first display area, a window corresponding to the selection area detected as the second

predetermined input on said second display area.

16. (Currently amended) A storage medium storing an information processing program

of an information processing apparatus comprising ~~storage programmed logic circuitry~~a memory

for storing data to display a plurality of windows and data to display a plurality of selection areas

which respectively correspond to said plurality of windows, and a display for including a first

display area on which only a predetermined window out of the plurality of windows is displayed

or the plurality of windows are displayed in an overlapping manner, and a second display area on

which said plurality of selection areas are displayed, wherein

said information processing program causes a processor of said information processing

apparatus to execute

detecting an input to display positions on said plurality of selection areas, and

displaying, when it is determined that a second predetermined input is performed at a

display position of a selection area corresponding to a window which is not displayed on said

first display area and said second display area or a window a part of which is hidden under a

forefront window out of the plurality of windows displayed in the overlapping manner on said

first display area, the window corresponding to the selection area detected as the second

predetermined input on said second display area.

17. (Currently amended) A window controlling method of an information processing apparatus comprising ~~storage programmed logic circuitry~~a memory for storing data to display a plurality of windows and data to display a plurality of selection areas which respectively correspond to said plurality of windows, and a display for including a first display area on which only a predetermined window out of the plurality of windows is displayed or the plurality of windows are displayed in an overlapping manner, and a second display area on which said plurality of selection areas are displayed, including:

detecting an input to a display position of said plurality of selection areas, and

displaying, when it is determined that a second predetermined input is performed at a display position of a selection area corresponding to a window which is not displayed on said first display area and said second display area or a window a part of which is hidden under the forefront window out of the plurality of windows displayed in the overlapping manner on said first display area, the window corresponding to the selection area on said second display area.

18. (Currently amended) An information processing apparatus, comprising:

~~storage programmed logic circuitry~~a memory for storing data to display a plurality of windows and data to display a plurality of selection areas which respectively correspond to said plurality of windows,

a display for including a first display area on which only a predetermined window out of the plurality of windows is displayed or the plurality of windows are displayed in an overlapping manner, and a second display area on which said plurality of selection areas are displayed,

a detector for detecting an input to display positions of said plurality of selection areas,

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and

a first display controller for displaying, when a predetermined input is performed within said selection area <u>corresponding to a window displayed</u> on said first display area by said detector, the window corresponding to the selection area on said second display area.

19. (New) An information processing system, comprising:

a memory for storing data to display a plurality of windows and data to display a plurality of selection areas which respectively correspond to said plurality of windows,

a display for including a first display area on which only a predetermined window out of the plurality of windows is displayed or the plurality of windows are displayed in an overlapping manner and a second display area on which said plurality of selection areas are displayed, and

a processor coupled to the memory, the memory storing instructions that, when executed by the processor, control the processor to:

detect an input to display positions of said plurality of selection areas, and

display, when it is determined that a first predetermined input is performed within a selection area corresponding to a window displayed on said first display area or a forefront window out of the plurality of windows displayed in the overlapping manner on said first display area by said detector, a window corresponding to the selection area that said detector detects as the first predetermined input on said second display area.